

For chip resistors bigger or equal to 1206 the recommendation is to have stand off. What is very important is that you tell them that the stand off shall be reproducible. This means that they shall have a process that allow to have always the same stand off. This is something I now request during the verification programme. You can also tell your students taht they shall have in house a dedicated procedure dedicated for such assembly.

For Melf usually there is not stand off. I do not request any.

One of the problem of the stand off is that it may cause problems of wetting as then during the assembly the termination being not in contact with the pad it reduce the temperature raise of the terminal.

The recommendation is the same for the LCCs devices.

I hear too often that the ECSS allow a stand off from present and 0.4 mm and therefore it is not necessary to have it under control..

Best Regards,

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